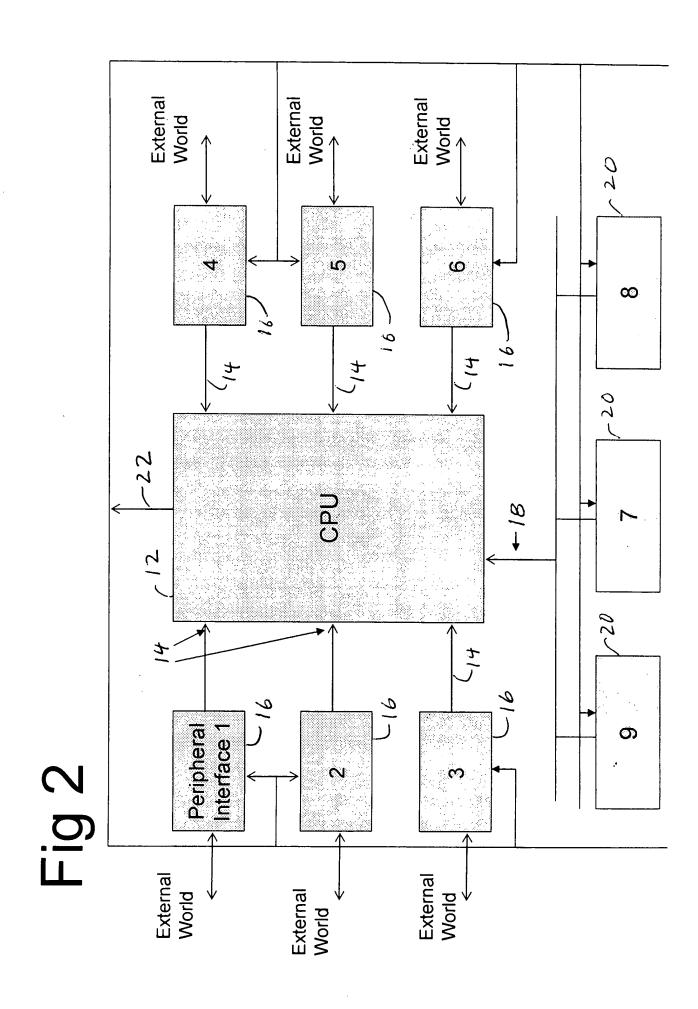
External World External World External World 70 ഹ ယ ∞ Fig 1: Prior Art 50 717 CPU 2 Peripheral Interface 1 တ Ø ന External World External World External World



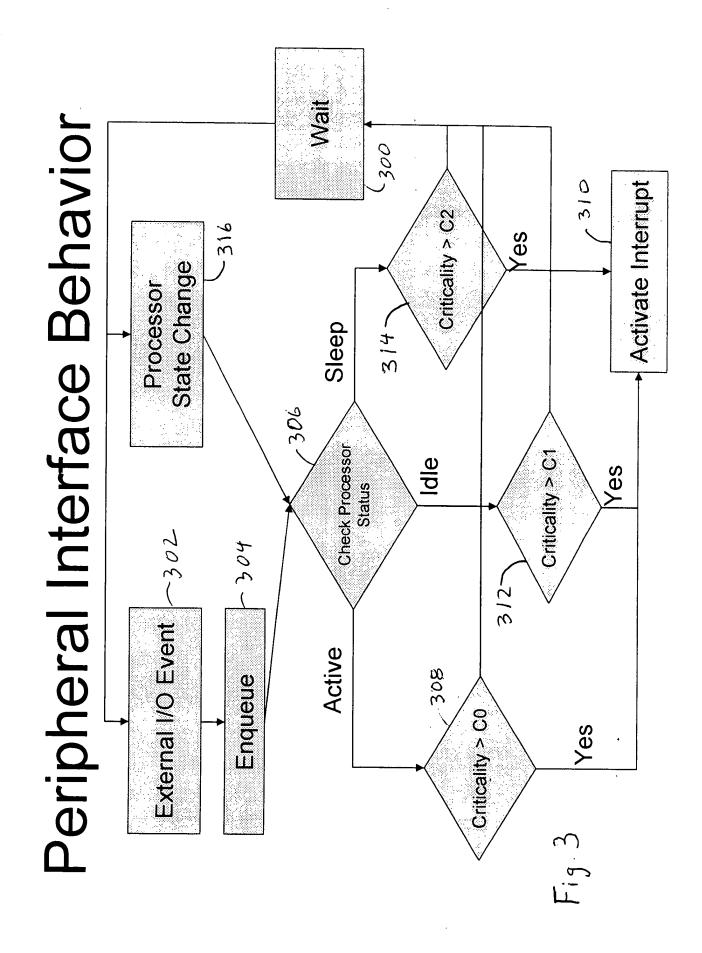


Fig. 4 Runtime Behavior

